



4125
JRW

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited
with the United States Postal Service with sufficient postage
as first class mail in an envelope addressed to: Mail Stop
Amendment, Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450 on November 21, 2007
(Date of Deposit)

Russell E. Fowler II

Name of person mailing Document or Fee

Russ Fowler

Signature

November 21, 2007

Date of Signature

Re: Application of: Berthold et al.
 Serial No.: 10/563,040
 Filed: December 30, 2005
 For: Master Latch Circuit with Signal Level
 Displacement for a Dynamic Flip Flop
 Confirmation No.: 6036
 Group Art Unit: 4125
 Examiner: Daniel E. Rojas
 Our Docket No.: 1890-0274

TRANSMITTAL

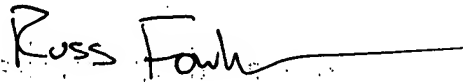
Please find for filing in connection with the above patent application the
following:

1. Response to Restriction Requirement; and
2. One (1) return post card.

Please charge any fee deficiency, or credit any overpayment, to Deposit Account No. 13-0014; but not to include any payment of issue fees.

Respectfully Submitted,

MAGINOT, MOORE & BECK

A handwritten signature in black ink, appearing to read "Russ Fowler", followed by a horizontal line.

November 21, 2007

Russell E. Fowler II
Registration No. 43,615
Chase Tower
111 Monument Circle, Suite 3250
Indianapolis, Indiana 46204-5109

Enclosures



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being
deposited with the United States Postal Service as
first class mail in an envelope addressed to:
Mail Stop Amendment, Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450 on
November 21, 2007

(Date of Deposit)

Russell E. Fowler II

Name of person mailing document or Fee

Signature

November 21, 2007

Date of Signature

Re: Application of: Berthold et al.
 Serial No.: 10/563,040
 Filed: December 30, 2005
 For: Master Latch Circuit with Signal Level
 Displacement for a Dynamic Flip Flop
 Confirmation No.: 6036
 Group Art Unit: 4125
 Examiner: Daniel E. Rojas
 Our Docket No.: 1890-0274

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated October 22, 2007 for the above-identified
patent application, please consider the following election.